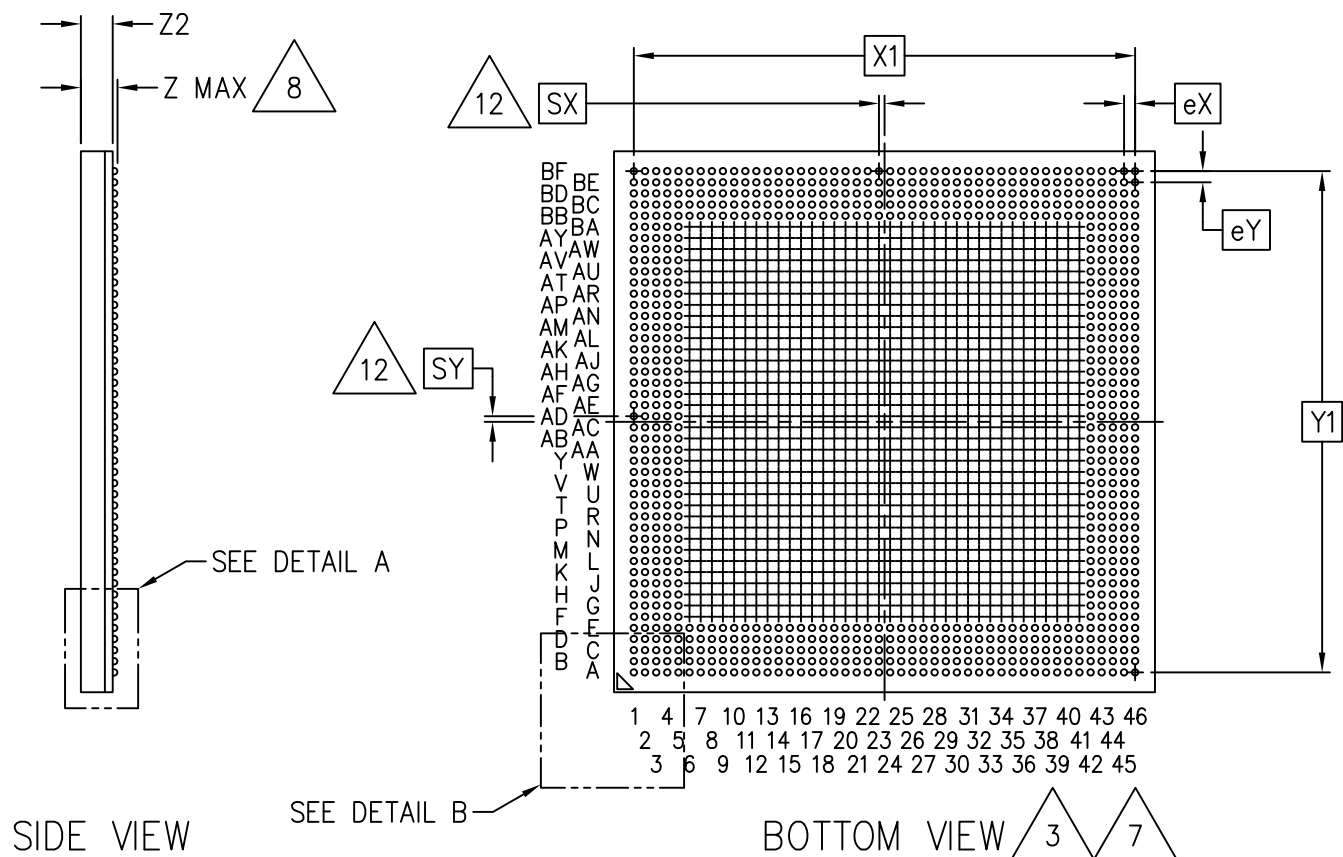
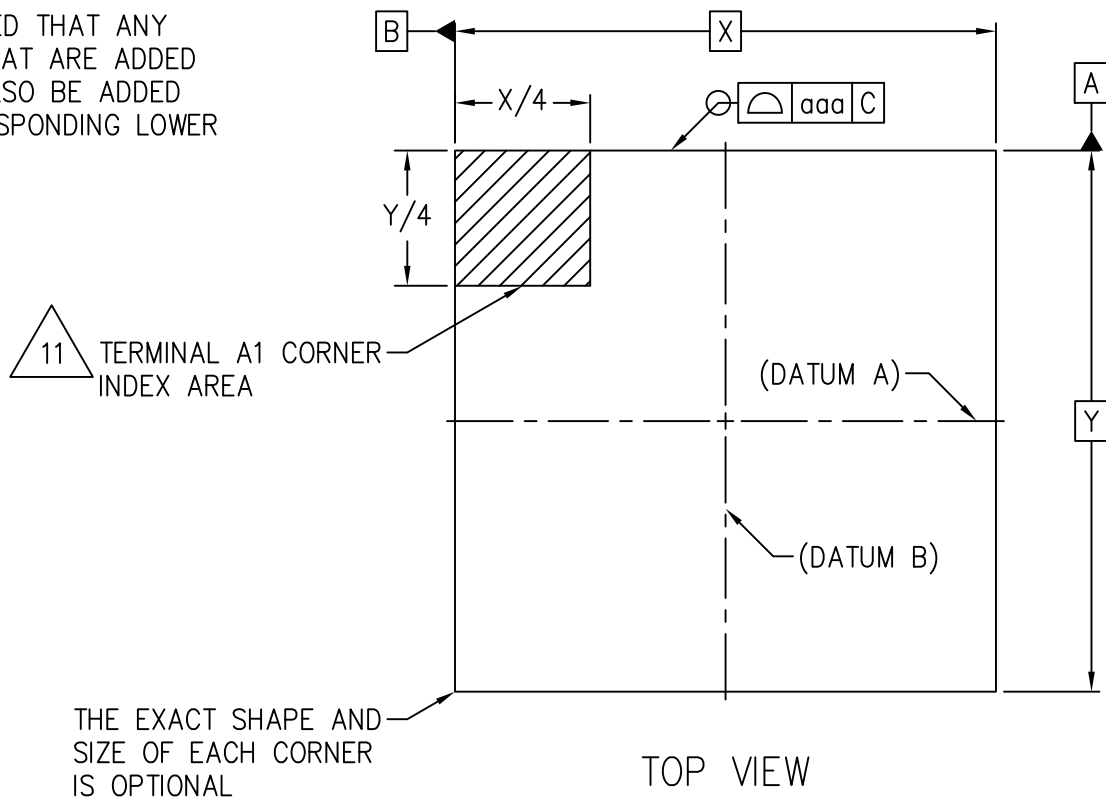


IT IS REQUESTED THAT ANY FOOTPRINTS THAT ARE ADDED TO THIS MO ALSO BE ADDED TO THE CORRESPONDING LOWER POP PACKAGE.



JEDEC SOLID STATE
PRODUCT OUTLINE

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THIS **REGISTERED OUTLINE** WAS PREPARED BY THE JEDEC JC-11 COMMITTEE AND REFLECTS A PRODUCT FOR ANTICIPATED USE IN THE ELECTRONICS INDUSTRY. CHANGES ARE LIKELY TO OCCUR. USERS ASSUME ALL RISK AND LIABILITY RESULTING FROM THE USE OF THIS OUTLINE.

TITLE PLASTIC BOTTOM GRID
ARRAY, BALL, 0.35 MM PITCH
SQ FAMILY PACKAGE (UPPER POP)

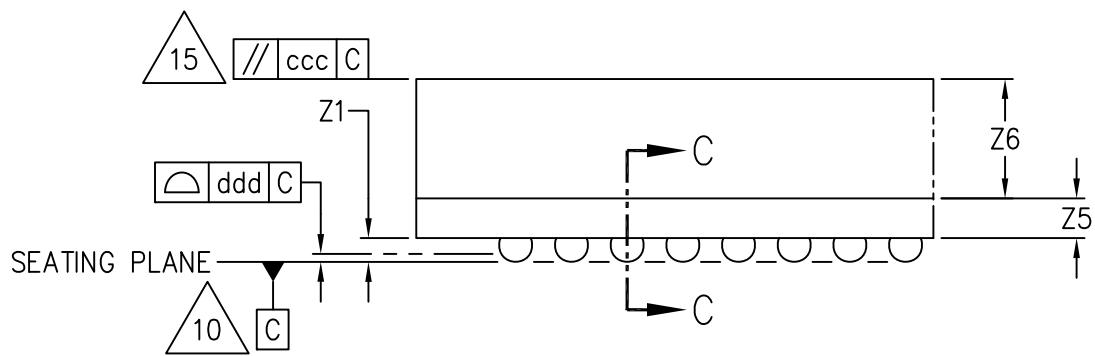
PACKAGE DESIGNATOR
PBGA-B#[#]
_I0p35...

ITEM
MO-367

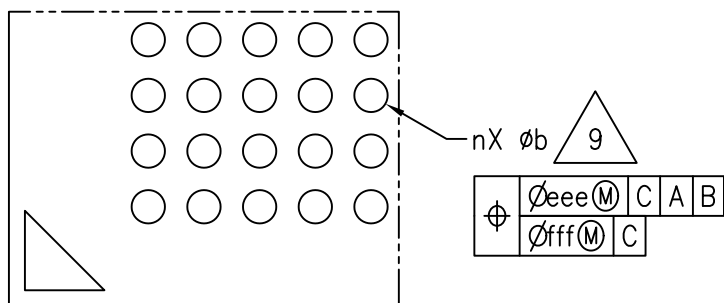
ISSUE
A

DATE
MAY 2025

SHEET
1 OF 9

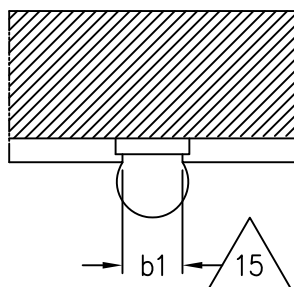


DETAIL A
(ROTATED 90° CW)

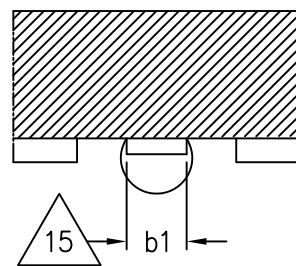


DETAIL B

TYPE 1 – SMD
(SOLDER MASK DEFINED)



TYPE 2 – NSMD
(NON SOLDER MASK DEFINED)



SECTION C-C

TABLE 1

COMMON DIMENSIONS		
SYMBOL		
Z		PACKAGE SPECIFIC
Z2	$b(\text{NOM}) = 0.225$	$Z2(\text{MAX}) = Z(\text{MAX}) - Z1$
Z5		OPTIONAL – PACKAGE SPECIFIC
Z6		OPTIONAL – PACKAGE SPECIFIC
e		0.35 BASIC
NOTES		2, 8
REF		11–1078
ISSUE		A

TABLE 2


COMMON DIMENSIONS										
SYMBOL		(b) SOLDER BALL DIAMETER								
		MIN	NOM	MAX	MIN	NOM	MAX	MIN	NOM	MAX
Z1		PACKAGE SPECIFIC	---	---	---	---	---	---	---	---
b		0.175	0.225	0.275	---	---	---	---	---	---
b1	TYPE1	0.15	---	---	---	---	---	---	---	---
	TYPE2	0.15	---	---	---	---	---	---	---	---
NOTES		2, 9, 17			—			—		
REF		11–1078			—			—		
ISSUE		A			—			—		

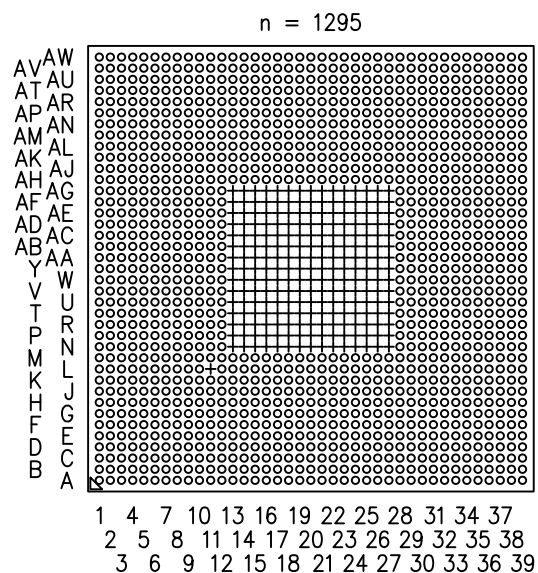
$$b1 = b(NOM) * 0.667$$

TABLE 3

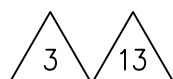
TOLERANCE OF FORM AND POSITION			
SYMBOL	PACKAGE TYPE	VALUE	
		b NOM = 0.225	
aaa	---	0.10	
ccc	ENCAPSULATED	0.20	
ddd	---	X OR Y	
		≤10.00	0.08
		>10.00, <14.00	0.10
		≥14.00	0.12
eee	ENCAPSULATED	0.15	
fff	---	0.05	
NOTES		1, 2	
REF		11–1078	
ISSUE		A	

TABLE 4

øb = 0.225 NOMINAL														
NEW VARIATION		X BASIC	Y BASIC	X1 BASIC	Y1 BASIC	MX	MY	SX BASIC	SY BASIC	n	N	TERMINAL PATTERN	REF	ISSUE
PBGA – B1295[1521]_I0p35– R14p0x14p0Z#–C0p225Z#		14.00	14.00	13.30	13.30	39	39	0.00	0.00	1295	1521	A	11–1078	A
NOTES		2	2	2	2	5	5	2, 12	2, 12	6, 13	6	13		



TERMINAL PATTERN A



+ = DEPOPULATED TERMINAL POSITIONS

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5–2009.
THIS OUTLINE CONFORMS TO JEP95, SECTION 4.22.
2. ALL DIMENSIONS ARE IN MILLIMETERS.
- 3 SOLDER BALL POSITION DESIGNATION PER JEP95 SECTION 3, SPP–010.
4. e REPRESENT THE SOLDER BALL GRID PITCH.
5. M_X AND M_Y REPRESENT THE MAXIMUM MATRIX SIZE CORRESPONDING TO THE X AND Y DISQIONS, RESPECTIVELY.
6. n REPRESENTS THE ACTUAL NUMBER OF SOLDER BALLS AFTER DEPOPLUATION.
 N REPRESENTS THE MAXIMUM NUMBER OF SOLDER BALLS FOR A FULL MATRIX, $M_D \times M_E$.

7 A DEPOPULATED 46 X 46 MATRIX SIZE IS SHOWN FOR ILLUSTRATION ONLY.

8 DIMENSION Z INCLUDES STANDOFF HEIGHT Z_1 , PACKAGE BODY THICKNESS AND LID HEIGHT, BUT DOES NOT INCLUDE ATTACHED FEATURES, e.g. EXTERNAL HEAT SINK. AN INTEGRAL HEAT SLUG IS NOT CONSIDERED AN ATTACHED FEATURE.

9 DIMENSION b IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER PARALLEL TO PRIMARY DATUM C.

10 PRIMARY DATUM C (SEATING PLANE) IS DEFINED BY THE PLANE ESTABLISHED BY THE CONTACT POINTS OF THREE OR MORE SOLDER BALLS THAT SUPPORT THE DEVICE WHEN IT IS PLACED ON TOP OF A PLANAR SURFACE.

11 THE A1 TERMINAL CORNER MUST BE IDENTIFIED ON BOTH THE BOTTOM AND TOP SIDES OF THE PACKAGE. THE IDENTIFICATION FEATURE CAN BE MADE USING INK, METALIZED MARKINGS, INDENTATIONS, OR OTHER FEATURES.

12 DIMENSIONS S_X AND S_Y ARE MEASURED WITH RESPECT TO DATUMS A AND B AND DEFINE THE POSITION OF THE CENTER SOLDER BALLS.
WHEN THERE IS AN ODD NUMBER OF SOLDER BALLS, S_X OR $S_Y = 0.00$
WHEN THERE IS AN EVEN NUMBER OF SOLDER BALLS, $S_X = e_X/2$ OR $S_Y = e_Y/2$.

13 SOLDER BALL DEPOPULATION IS ALLOWED. DEPOPULATION IS THE OMISSION OF SOLDER BALLS FROM A FULL MATRIX ($M_X \times M_Y$).

14 THE SOLDERABLE SURFACE MAY BE DEFINED BY AN OPENING IN THE SOLDER RESIST LAYER (TYPE 1) OR BY THE SIZE OF A METALIZED PAD (TYPE 2). IT MAY BE ELLIPITACL PROVIDED THE RATIO OF THE MAJOR TO MINOR AXIS IS NO GREATER THAN 2/1, AND THE SURFACE AREA IS NO LESS THEN THE MINIMUM FOR A CIRCULAR PAD. FOR TYPE 2 DESIGNS, EXPOSED COPPER TRACES ARE PERMITTED OUTSIDE THE b_1 PAD AREA.

NOTES CONTINUED:

15 FOR GLOB TOP AND FLIP CHIP CONFIGURATIONS, PARALLELISM (ccc) APPLIES ONLY TO THE SURFACE DISQLY ABOVE THE DIE AREA. THE PARALLELISM SPECIFICALLY WILL NOT APPLY TO ANY FILLET OR SLOPED REGION OF THE ENCAPSULANT.

16 SEE JESD30 FOR EXPLANATION OF VARIATION SCHEME.
PACKAGE HEIGHT IS THE MAXIMUM PACKAGE THICKNESS.

17. THE Z1 HEIGHT NEEDS TO BE ACCEPTABLE TO MANUFACTURING STANDARDS.

STP (3D) FILE RECORD

3D FILE NAMES MAY EXCEED LENGTH REQUIREMENTS FOR SOME SOFTWARE TOOLS.

STP FILE NAME	DATE	ITEM NUMBER
MO-367A_PBGA-B1295[1521]_I0p35-R14p0X14p0Z#-C0p225Z#	MAY 2025	11-1078

TASK GROUP CONTRIBUTORS

SAMSUNG

CHANGE RECORD

IF THE CHANGE INVOLVES ANY WORDS ADDED OR DELETED (EXCLUDING DELETION OF ACCIDENTALLY REPEATED WORDS), THE CHANGE IS TO BE INCLUDED BELOW. PUNCTUATION CHANGES MAY OR MAY NOT BE INCLUDED.

INITIAL ISSUE: A	DATE: MAY 2025	ITEM NUMBER: 11-1078
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CHANGE RECORD HISTORY:

ISSUE:	DATE:	ITEM NUMBER
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LOCATION:	CHANGED FROM:	CHANGED TO: